

Curriculum Vitae Dr.-Ing. Stephan Henzler



Stephan Henzler received the Dipl.-Ing. degree in electrical engineering in 2002, the Dr.-Ing. degree in 2006, and the habilitation¹ degree in 2010 from the Technische Universität München (TUM), Germany. From 2002 to 2005, he was with the Institute for Technical Electronics, Technische Universität München, where he worked on low-power digital integrated circuit design and leakage reduction techniques. For his dissertation on power management and leakage reduction techniques he received the Rhode-und-Schwarz outstanding thesis award 2007. In 2005, he joined the Advanced Systems and Circuits Department of Infineon Technologies AG, Munich, where he worked on high-speed/high-performance digital integrated circuits, variability in deep-submicron CMOS technologies, and mixed-signal circuit design in nanometer CMOS technologies, especially time-to-digital converters. In 2010 he joined the wireless mixed-signal department of Infineon where he works on integrated power management.

Stephan Henzler has authored or co-authored more than 30 technical publications, the Springer books “Power Management of Digital Circuits in Deep Sub-Micron CMOS Technologies” and “Time-to-Digital Converters”. He holds more than 20 international patents or pending patents. His research interests are currently focused on the digitalization of mixed-signal functionality, digital enhancement and calibration techniques, and time-domain signal representation. He is a visiting lecturer at Technische Universität München, Germany (TUM) for “High-Speed Digital CMOS Circuits” and “Mixed-Signal Electronics”. He is also a visiting lecturer in the joint master program between TUM and Nanyang Technological University (NTU) in Singapur where he teaches “Mixed-Signal Electronics”. He currently evaluates online teaching techniques and produces online versions of his lectures because he believes that this is a reasonable addition to live lectures in a globalized world with diminishing time resources.

Dr. Henzler is a member of the German Association of Electrical Engineers (VDE) and the IEEE. He is a member of the technical program committee of the European Solid State Circuits Conference (ESSCIRC).

¹ postdoctoral lecture qualification