

Diploma-, Master-, and Bachelorthesis

Dr. Stephan Henzler, August 31, 2009

1. D. Jostin, S. Henzler and D. Schmitt-Landsiedel, "Improvement of Linearity in High-Resolution Time-to-Digital Converters", Masterthesis, 9/2009, Institute for Technical Electronics, Technische Universitaet Muenchen.
2. A. Sarrazin, S. Henzler, "Design Automation of Time-to-Digital Converter Systems", Masterthesis, 8/2009, Ecole D'Ingenieurs isen, Lille.
3. S. Ziegmeier, S. Henzler and D. Schmitt-Landsiedel, "High-Speed Serial On-Chip Signaling", Diplomathesis, 4/2009, Institute for Technical Electronics, Technical University Munich.
4. P. Sockalingam, S. Henzler and D. Schmitt-Landsiedel, "Robust Design of Keeper Circuits in High-Speed Dynamic Logic", Masterthesis, 2/2008, Institute for Technical Electronics, German Institute of Science and Technology.
5. S. Sahasrabudhe, S. Henzler and D. Schmitt-Landsiedel, "Timing Modeling and Verification of Digital Macro Interfaces including Multi-clock Registers", Masterthesis, 2/2008, Institute for Technical Electronics, German Institute of Science and Technology.
6. D. Lorenz, S. Henzler and D. Schmitt-Landsiedel, "Untersuchung von Time-to-Digital Konvertern als Phasendetektoren in voll-digitalen PLLs", Diplomathesis, 3/2007, Institute for Technical Electronics, Technical University Munich.
7. C. Kampen, S. Henzler and D. Schmitt-Landsiedel, "Statistische Untersuchung von Low-Power Schaltungstechniken", Diplomathesis, 4/2006, Institute for Technical Electronics, Technical University Munich.
8. S. Hausmann, S. Henzler and D. Schmitt-Landsiedel, "Power Gating of Multi-VDD Circuits for Low Power and Variation Aware Design", Diplomathesis, 10/2005, Institute for Technical Electronics, Technical University Munich.
9. M. Eireiner, S. Henzler and D. Schmitt-Landsiedel, "Timing Violations due to VDD/VSS Bounce", Diplomathesis, 9/2005, Institute for Technical Electronics, Technical University Munich.
10. D. Comarella, S. Henzler and D. Schmitt-Landsiedel, ", Bachelorthesis, Institute for Technical Electronics, Technical University Munich.
11. M. Eireiner, S. Henzler, R. Goettsche, and D. Schmitt-Landsiedel, "Evaluation of Alternative High-Performance Logic Design Styles for Ultra-Deep Submicron CMOS Technologies", Bachelorthesis, 2/2005, Institute for Technical Electronics, Technical University Munich.
12. P. Spindler, S. Henzler and D. Schmitt-Landsiedel, "Differential Evolution for Flip-Flops", Diplomathesis, 4/2005, Institute for Technical Electronics, Technical University Munich.
13. M. Schmidl, M. Roesler, S. Henzler, and D. Schmitt-Landsiedel, "Entwicklung einer Steuer- und Leistungselektronik mit Mikrocontrolleranbindung zur dynamischen Bord-

- netzunterstuetzung”, Diplomathesis, 4/2005, Institute for Technical Electronics, Technical University Munich.
14. F. Bauer, S. Henzler, C. Pacha, and D. Schmitt-Landsiedel, “Charakterisierung von leckstromarmen CMOS Schaltungskonzepten mit Sleep-Transistor”, Diplomathesis, 12/2004, Institute for Technical Electronics, Technical University Munich.
 15. T. Reichthalhammer, S. Henzler and D. Schmitt-Landsiedel, “Investigation of IR-Drop in Power Switched Circuits”, Bachelorthesis, 8/2004, Institute for Technical Electronics, Technical University Munich.
 16. S. Skiathitis, S. Henzler and D. Schmitt-Landsiedel, “Evaluierung verschiedener Low-Power Schaltungstechniken am Beispiel arithmetischer Makros”, Diplomathesis, 1/2004, Institute for Technical Electronics, Technical University Munich.
 17. P. Spindler, S. Henzler and D. Schmitt-Landsiedel, “Evaluation Strategy of System Scenarios for Flip-Flops”, Bachelorthesis, 1/2004, Institute for Technical Electronics, Technical University Munich.
 18. M. Reinl, S. Henzler and D. Schmitt-Landsiedel, “Verlustleistungsreduzierung in CMOS Schaltungen mit Hilfe verschiedener Versorgungsspannungen”, Diplomathesis, 10/2003, Institute for Technical Electronics, Technical University Munich.
 19. M. Koban, S. Henzler and D. Schmitt-Landsiedel, “ZigZag Super Cut-Off CMOS, Anwendung und Grenzen”, Bachelorthesis, 5/2003, Institute for Technical Electronics, Technical University Munich.