

Master's Thesis

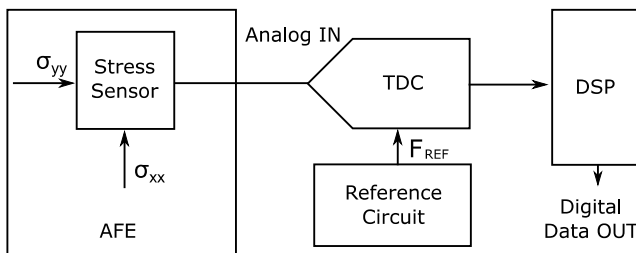
Design of a Time-to-Digital Converter for Mechanical Stress Sensors

Background

Mechanical stress can be very important when designing precision analog and mixed-signal circuits. TUM and Texas Instruments set a common goal to develop ICs for sensing the stress and to compensate its effects on critical circuit parameters.

Your tasks

- Development of following circuit components:
 - Analog Front End (AFE)
 - Time-to-Digital Converter (TDC)
 - Digital Signal Processor (DSP)



You will have access to TI's BICMOS Technology and Design Environment.

Your profile

- Solid knowledge in analog and mixed-signal IC design fundamentals
- Experience with Cadence and MATLAB
- Ability to work both in a team as well as independently
- High motivation for the design of analog and mixed-signal ICs

Interested? Don't hesitate to ask or apply directly per mail

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